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DEC 09 2002 SC154
PATENT & TRADEMARK OFFICE

Docket No.: 49657-904

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Yoshinori TANAKA, et al. :
Serial No.: 09/756,864 : Group Art Unit: 2826
Filed: January 10, 2001 : Examiner: Thomas L. Dickey
For: SEMICONDUCTOR DEVICE COMPRISING A HIGHLY-RELIABLE,
CONSTANT CAPACITANCE CAPACITOR (AS AMENDED)

#13/F
Amst
PATENT

J. McMillan
117/03
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TECHNOLOGY CENTER 2800
DEC 11 2002

AMENDMENT

Commissioner for Patents
Washington, DC 20231

Sir:

The following amendment and remarks are submitted in response to the June 17, 2002
non-Final Office Action.

IN THE CLAIMS:

Please cancel claim 14. Please amend claim 12 as follows and add new claim 25. A
clean copy of the claims is appended hereto.

12. (Thrice Amended) A semiconductor device including a memory cell region and a
peripheral circuit region, comprising:

a semiconductor substrate having a major surface;

an insulating film, having an upper surface, being formed on said major surface of said
semiconductor substrate to extend from said memory cell region to said peripheral circuit region,